# **Lecture 1:**

1. Server Consolidation: CPU, memory and I/o virtualization
2. Control Sensitive and Behavior Sensitive Instructions
3. Virtualizable Non-Virtualizable CPU, x86 and mainframe,
4. Trap and Emulate model, issues of trap and emulate model

# **Lecture 2:**

1. Binary Translation
2. Para Virtualization
3. Hardware-assisted Virtualization
4. Interrupt handling in Binary translation, paravirtualization and hardware-assisted virtualization