

SECTION 4-3
CHECKUP

1. Define β_{DC} and α_{DC} . What is h_{FE} ?
2. If the dc current gain of a transistor is 100, determine β_{DC} and α_{DC} .
3. What two variables are plotted on a collector characteristic curve?
4. What bias conditions must exist for a transistor to operate as an amplifier?
5. Does β_{DC} increase or decrease with temperature?
6. For a given type of transistor, can β_{DC} be considered to be a constant?

4-4 THE BJT AS AN AMPLIFIER

Amplification is the process of linearly increasing the amplitude of an electrical signal and is one of the major properties of a transistor. As you learned, a BJT exhibits current gain (called β). When a BJT is biased in the active (or linear) region, as previously described, the BE junction has a low resistance due to forward bias and the BC junction has a high resistance due to reverse bias.

After completing this section, you should be able to

- Discuss how a BJT is used as a voltage amplifier
- List the dc and ac quantities in an amplifier
 - ♦ Describe how the dc and ac quantities are identified
- Describe voltage amplification
 - ♦ Draw the schematic for a basic BJT amplifier
 - ♦ Define *current gain* and *voltage gain*
 - ♦ Calculate voltage gain
 - ♦ Calculate amplifier output voltage

DC and AC Quantities

Before discussing the concept of transistor amplification, the designations that we will use for the circuit quantities of current, voltage, and resistance must be explained because amplifier circuits have both dc and ac quantities.

In this text, italic capital letters are used for both dc and ac currents (I) and voltages (V). This rule applies to rms, average, peak, and peak-to-peak ac values. AC current and voltage values are always rms unless stated otherwise. Although some texts use lowercase i and v for ac current and voltage, we reserve the use of lowercase i and v only for instantaneous values. In this text, the distinction between a dc current or voltage and an ac current or voltage is in the subscript.

DC quantities always carry an uppercase roman (nonitalic) subscript. For example, I_B , I_C , and I_E are the dc transistor currents. V_{BE} , V_{CB} , and V_{CE} are the dc voltages from one transistor terminal to another. Single subscripted voltages such as V_B , V_C , and V_E are dc voltages from the transistor terminals to ground.

AC and all time-varying quantities always carry a lowercase italic subscript. For example, I_b , I_c , and I_e are the ac transistor currents. V_{be} , V_{cb} , and V_{ce} are the ac voltages from one transistor terminal to another. Single subscripted voltages such as V_b , V_c , and V_e are ac voltages from the transistor terminals to ground.

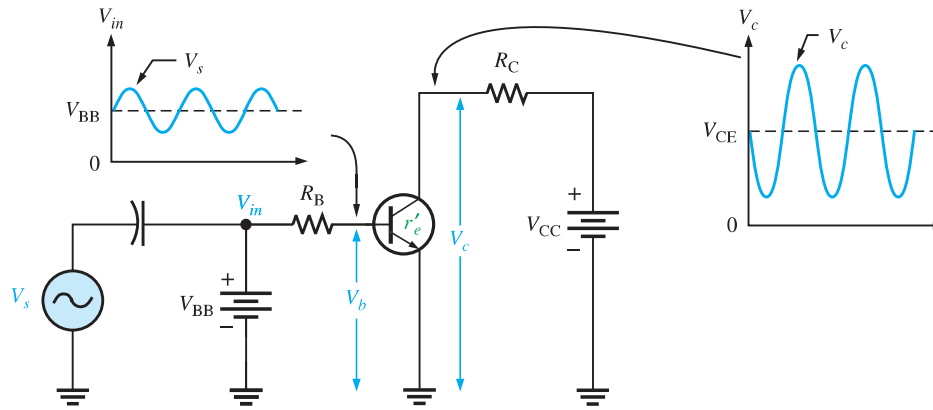
The rule is different for *internal* transistor resistances. As you will see later, transistors have internal ac resistances that are designated by lowercase r' with an appropriate subscript. For example, the internal ac emitter resistance is designated as r'_e .

Circuit resistances external to the transistor itself use the standard italic capital R with a subscript that identifies the resistance as dc or ac (when applicable), just as for current and voltage. For example R_E is an external dc emitter resistance and R_e is an external ac emitter resistance.

Voltage Amplification

As you have learned, a transistor amplifies current because the collector current is equal to the base current multiplied by the current gain, β . The base current in a transistor is very small compared to the collector and emitter currents. Because of this, the collector current is approximately equal to the emitter current.

With this in mind, let's look at the circuit in Figure 4–21. An ac voltage, V_s , is superimposed on the dc bias voltage V_{BB} by capacitive coupling as shown. The dc bias voltage V_{CC} is connected to the collector through the collector resistor, R_C .



◀ FIGURE 4–21

Basic transistor amplifier circuit with ac source voltage V_s and dc bias voltage V_{BB} superimposed.

The ac input voltage produces an ac base current, which results in a much larger ac collector current. The ac collector current produces an ac voltage across R_C , thus producing an amplified, but inverted, reproduction of the ac input voltage in the active region of operation, as illustrated in Figure 4–21.

The forward-biased base-emitter junction presents a very low resistance to the ac signal. This internal ac emitter resistance is designated r'_e in Figure 4–21 and appears in series with R_B . The ac base voltage is

$$V_b = I_e r'_e$$

The ac collector voltage, V_c , equals the ac voltage drop across R_C .

$$V_c = I_c R_C$$

Since $I_c \cong I_e$, the ac collector voltage is

$$V_c \cong I_e R_C$$

V_b can be considered the transistor ac input voltage where $V_b = V_s - I_b R_B$. V_c can be considered the transistor ac output voltage. Since *voltage gain* is defined as the ratio of the output voltage to the input voltage, the ratio of V_c to V_b is the ac voltage gain, A_v , of the transistor.

$$A_v = \frac{V_c}{V_b}$$

Substituting $I_e R_C$ for V_c and $I_e r'_e$ for V_b yields

$$A_v = \frac{V_c}{V_b} \cong \frac{I_e R_C}{I_e r'_e}$$

The I_e terms cancel; therefore,

$$A_v \cong \frac{R_C}{r'_e}$$

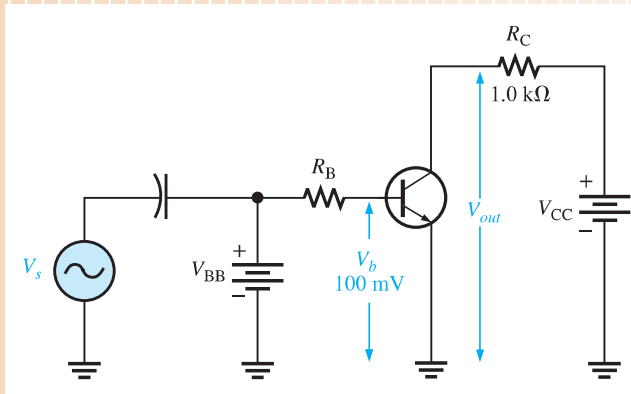
Equation 4–7

Equation 4–7 shows that the transistor in Figure 4–21 provides amplification in the form of voltage gain, which is dependent on the values of R_C and r'_e .

Since R_C is always considerably larger in value than r'_e , the output voltage for this configuration is greater than the input voltage. Various types of amplifiers are covered in detail in later chapters.

EXAMPLE 4–9▶ **FIGURE 4–22**

Determine the voltage gain and the ac output voltage in Figure 4–22 if $r'_e = 50 \Omega$.



Solution The voltage gain is

$$A_v \cong \frac{R_C}{r'_e} = \frac{1.0 \text{ k}\Omega}{50 \Omega} = \mathbf{20}$$

Therefore, the ac output voltage is

$$V_{out} = A_v V_b = (20)(100 \text{ mV}) = \mathbf{2 \text{ V rms}}$$

Related Problem What value of R_C in Figure 4–22 will it take to have a voltage gain of 50?

**SECTION 4–4
CHECKUP**

1. What is amplification?
2. How is voltage gain defined?
3. Name two factors that determine the voltage gain of an amplifier.
4. What is the voltage gain of a transistor amplifier that has an output of 5 V rms and an input of 250 mV rms?
5. A transistor connected as in Figure 4–22 has an $r'_e = 20 \Omega$. If R_C is 1200 Ω , what is the voltage gain?

4–5 THE BJT AS A SWITCH

In the previous section, you saw how a BJT can be used as a linear amplifier. The second major application area is switching applications. When used as an electronic switch, a BJT is normally operated alternately in cutoff and saturation. Many digital circuits use the BJT as a switch.

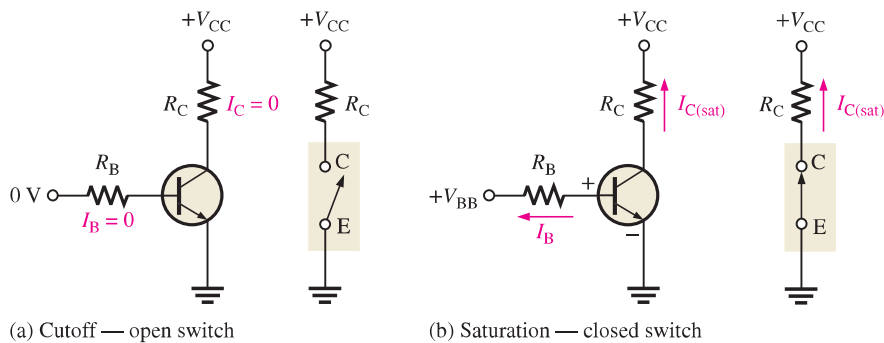
After completing this section, you should be able to

- **Discuss how a BJT is used as a switch**
- Describe BJT switching operation
- Explain the conditions in cutoff
 - ♦ Determine the cutoff voltage in terms of the dc supply voltage

- Explain the conditions in saturation
 - ♦ Calculate the collector current and the base current in saturation
- Describe a simple application

Switching Operation

Figure 4–23 illustrates the basic operation of a BJT as a switching device. In part (a), the transistor is in the cutoff region because the base-emitter junction is not forward-biased. In this condition, there is, ideally, an *open* between collector and emitter, as indicated by the switch equivalent. In part (b), the transistor is in the saturation region because the base-emitter junction and the base-collector junction are forward-biased and the base current is made large enough to cause the collector current to reach its saturation value. In this condition, there is, ideally, a *short* between collector and emitter, as indicated by the switch equivalent. Actually, a small voltage drop across the transistor of up to a few tenths of a volt normally occurs, which is the saturation voltage, $V_{CE(sat)}$.



◀ **FIGURE 4–23**
Switching action of an ideal transistor.

Conditions in Cutoff As mentioned before, a transistor is in the cutoff region when the base-emitter junction is not forward-biased. Neglecting leakage current, all of the currents are zero, and V_{CE} is equal to V_{CC} .

$$V_{CE(\text{cutoff})} = V_{CC}$$

Equation 4–8

Conditions in Saturation As you have learned, when the base-emitter junction is forward-biased and there is enough base current to produce a maximum collector current, the transistor is saturated. The formula for collector saturation current is

$$I_{C(\text{sat})} = \frac{V_{CC} - V_{CE(\text{sat})}}{R_C}$$

Equation 4–9

Since $V_{CE(\text{sat})}$ is very small compared to V_{CC} , it can usually be neglected.

The minimum value of base current needed to produce saturation is

$$I_{B(\text{min})} = \frac{I_{C(\text{sat})}}{\beta_{DC}}$$

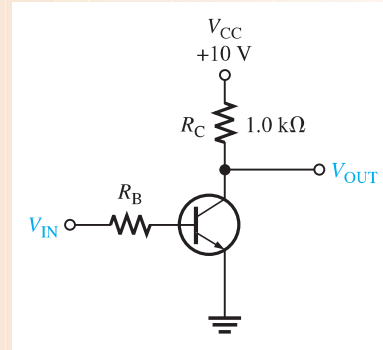
Equation 4–10

Normally, I_B should be significantly greater than $I_{B(\text{min})}$ to ensure that the transistor is saturated.

EXAMPLE 4–10

- (a) For the transistor circuit in Figure 4–24, what is V_{CE} when $V_{IN} = 0$ V?
- (b) What minimum value of I_B is required to saturate this transistor if β_{DC} is 200? Neglect $V_{CE(\text{sat})}$.
- (c) Calculate the maximum value of R_B when $V_{IN} = 5$ V.

▶ FIGURE 4-24



Solution (a) When $V_{IN} = 0$ V, the transistor is in cutoff (acts like an open switch) and

$$V_{CE} = V_{CC} = 10 \text{ V}$$

(b) Since $V_{CE(\text{sat})}$ is neglected (assumed to be 0 V),

$$I_{C(\text{sat})} = \frac{V_{CC}}{R_C} = \frac{10 \text{ V}}{1.0 \text{ k}\Omega} = 10 \text{ mA}$$

$$I_{B(\text{min})} = \frac{I_{C(\text{sat})}}{\beta_{DC}} = \frac{10 \text{ mA}}{200} = 50 \mu\text{A}$$

This is the value of I_B necessary to drive the transistor to the point of saturation. Any further increase in I_B will ensure the transistor remains in saturation but there cannot be any further increase in I_C .

(c) When the transistor is on, $V_{BE} \cong 0.7$ V. The voltage across R_B is

$$V_{R_B} = V_{IN} - V_{BE} \cong 5 \text{ V} - 0.7 \text{ V} = 4.3 \text{ V}$$

Calculate the maximum value of R_B needed to allow a minimum I_B of $50 \mu\text{A}$ using Ohm's law as follows:

$$R_{B(\text{max})} = \frac{V_{R_B}}{I_{B(\text{min})}} = \frac{4.3 \text{ V}}{50 \mu\text{A}} = 86 \text{ k}\Omega$$

Related Problem Determine the minimum value of I_B required to saturate the transistor in Figure 4-24 if β_{DC} is 125 and $V_{CE(\text{sat})}$ is 0.2 V.

A Simple Application of a Transistor Switch

The transistor in Figure 4-25 is used as a switch to turn the LED on and off. For example, a square wave input voltage with a period of 2 s is applied to the input as indicated. When

▶ FIGURE 4-25

A transistor used to switch an LED on and off.

