$$h(t) = \begin{cases} 0, & t < 0 \\ -4, & 0 < t < 2 \\ 3t - 8, & 2 < t < 6 \\ 0, & t > 6 \end{cases}$$

express h(t) in terms of the singularity functions.

Answer:
$$-4u(t) + 2u(t-2) + 3r(t-2) - 10u(t-6) - 3r(t-6)$$
.

Evaluate the following integrals involving the impulse function:

$$\int_{0}^{10} (t^{2} + 4t - 2)\delta(t - 2)dt$$
$$\int_{-\infty}^{\infty} [\delta(t - 1)e^{-t}\cos t + \delta(t + 1)e^{-t}\sin t]dt$$

Solution:

For the first integral, we apply the sifting property in Eq. (7.32).

$$\int_{0}^{10} (t^{2} + 4t - 2)\delta(t - 2)dt = (t^{2} + 4t - 2)|_{t=2} = 4 + 8 - 2 = 10$$

Similarly, for the second integral,

$$\int_{-\infty}^{\infty} \left[\delta(t-1)e^{-t}\cos t + \delta(t+1)e^{-t}\sin t\right]dt$$

= $e^{-t}\cos t|_{t=1} + e^{-t}\sin t|_{t=-1}$
= $e^{-1}\cos 1 + e^{1}\sin(-1) = 0.1988 - 2.2873 = -2.0885$

Evaluate the following integrals:

$$\int_{-\infty}^{\infty} (t^3 + 5t^2 + 10)\delta(t+3)dt, \qquad \int_{0}^{10} \delta(t-\pi)\cos 3t \, dt$$

Answer: 28, −1.

7.5 Step Response of an *RC* Circuit

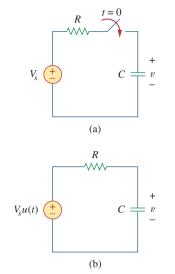
When the dc source of an *RC* circuit is suddenly applied, the voltage or current source can be modeled as a step function, and the response is known as a *step response*.

The **step response** of a circuit is its behavior when the excitation is the step function, which may be a voltage or a current source.

Practice Problem 7.9

Example 7.9

Practice Problem 7.8



The step response is the response of the circuit due to a sudden application of a dc voltage or current source.

Consider the *RC* circuit in Fig. 7.40(a) which can be replaced by the circuit in Fig. 7.40(b), where V_s is a constant dc voltage source. Again, we select the capacitor voltage as the circuit response to be determined. We assume an initial voltage V_0 on the capacitor, although this is not necessary for the step response. Since the voltage of a capacitor cannot change instantaneously,

$$v(0^{-}) = v(0^{+}) = V_0$$
(7.40)

where $v(0^-)$ is the voltage across the capacitor just before switching and $v(0^+)$ is its voltage immediately after switching. Applying KCL, we have

 $C\frac{dv}{dt} + \frac{v - V_s u(t)}{R} = 0$

or

$$\frac{dv}{dt} + \frac{v}{RC} = \frac{V_s}{RC}u(t)$$
(7.41)

where v is the voltage across the capacitor. For t > 0, Eq. (7.41) becomes

$$\frac{dv}{dt} + \frac{v}{RC} = \frac{V_s}{RC}$$
(7.42)

Rearranging terms gives

$$\frac{dv}{dt} = -\frac{v - V_s}{RC}$$
$$\frac{dv}{v - V_s} = -\frac{dt}{RC}$$
(7.43)

Integrating both sides and introducing the initial conditions,

$$\ln(v - V_s) \Big|_{V_0}^{v(t)} = -\frac{t}{RC} \Big|_0^t$$
$$\ln(v(t) - V_s) - \ln(V_0 - V_s) = -\frac{t}{RC} + 0$$

or

or

$$\ln \frac{v - V_s}{V_0 - V_s} = -\frac{t}{RC}$$
(7.44)

Taking the exponential of both sides

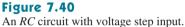
$$\frac{v - V_s}{V_0 - V_s} = e^{-t/\tau}, \qquad \tau = RC$$
$$v - V_s = (V_0 - V_s)e^{-t/\tau}$$

or

$$v(t) = V_s + (V_0 - V_s)e^{-t/\tau}, \quad t > 0$$
 (7.45)

Thus,

$$v(t) = \begin{cases} V_0, & t < 0\\ V_s + (V_0 - V_s)e^{-t/\tau}, & t > 0 \end{cases}$$
(7.46)



This is known as the *complete response* (or total response) of the *RC* circuit to a sudden application of a dc voltage source, assuming the capacitor is initially charged. The reason for the term "complete" will become evident a little later. Assuming that $V_s > V_0$, a plot of v(t) is shown in Fig. 7.41.

If we assume that the capacitor is uncharged initially, we set $V_0 = 0$ in Eq. (7.46) so that

$$v(t) = \begin{cases} 0, & t < 0\\ V_s(1 - e^{-t/\tau}), & t > 0 \end{cases}$$
(7.47)

which can be written alternatively as

$$v(t) = V_{\rm s}(1 - e^{-t/\tau})u(t)$$
(7.48)

This is the complete step response of the RC circuit when the capacitor is initially uncharged. The current through the capacitor is obtained from Eq. (7.47) using i(t) = C dv/dt. We get

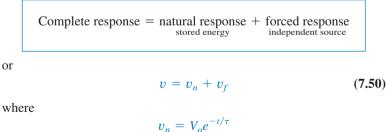
$$i(t) = C \frac{dv}{dt} = \frac{C}{\tau} V_s e^{-t/\tau}, \quad \tau = RC, \quad t > 0$$

or

$$(t) = \frac{V_s}{R} e^{-t/\tau} u(t)$$
 (7.49)

Figure 7.42 shows the plots of capacitor voltage v(t) and capacitor current i(t).

Rather than going through the derivations above, there is a systematic approach-or rather, a short-cut method-for finding the step response of an RC or RL circuit. Let us reexamine Eq. (7.45), which is more general than Eq. (7.48). It is evident that v(t) has two components. Classically there are two ways of decomposing this into two components. The first is to break it into a "natural response and a forced response" and the second is to break it into a "transient response and a steady-state response." Starting with the natural response and forced response, we write the total or complete response as

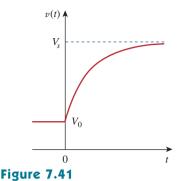


and

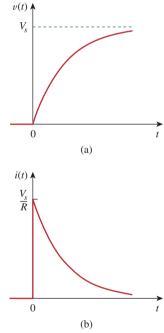
or

$$v_f = V_s (1 - e^{-t/\tau})$$

We are familiar with the natural response v_n of the circuit, as discussed in Section 7.2. v_f is known as the *forced* response because it is produced by the circuit when an external "force" (a voltage source in this case) is applied. It represents what the circuit is forced to do by the input excitation. The natural response eventually dies out along with the transient component of the forced response, leaving only the steadystate component of the forced response.



Response of an RC circuit with initially charged capacitor.





Step response of an RC circuit with initially uncharged capacitor: (a) voltage response, (b) current response.

Another way of looking at the complete response is to break into two components—one temporary and the other permanent, i.e.,

Complete response = transient response + steady-state response temporary part + steady-state response

v

or

$$= v_t + v_{ss} \tag{7.51}$$

(7.52a)

where

and

$$v_{ss} = V_s \tag{7.52b}$$

The *transient response* v_t is temporary; it is the portion of the complete response that decays to zero as time approaches infinity. Thus,

 $v_t = (V_o - V_s)e^{-t/\tau}$

The **transient response** is the circuit's temporary response that will die out with time.

The *steady-state response* v_{ss} is the portion of the complete response that remains after the transient reponse has died out. Thus,

The **steady-state response** is the behavior of the circuit a long time after an external excitation is applied.

The first decomposition of the complete response is in terms of the source of the responses, while the second decomposition is in terms of the permanency of the responses. Under certain conditions, the natural response and transient response are the same. The same can be said about the forced response and steady-state response.

Whichever way we look at it, the complete response in Eq. (7.45) may be written as

$$v(t) = v(\infty) + [v(0) - v(\infty)]e^{-t/\tau}$$
 (7.53)

where v(0) is the initial voltage at $t = 0^+$ and $v(\infty)$ is the final or steadystate value. Thus, to find the step response of an *RC* circuit requires three things:

- 1. The initial capacitor voltage v(0).
- 2. The final capacitor voltage $v(\infty)$.
- 3. The time constant τ .

Once we know $x(0), x(\infty)$, and τ , almost all the circuit problems in this chapter can be solved using the formula

 $x(t) = x(\infty) + [x(0) - x(\infty)]e^{-t/\tau}$

We obtain item 1 from the given circuit for t < 0 and items 2 and 3 from the circuit for t > 0. Once these items are determined, we obtain

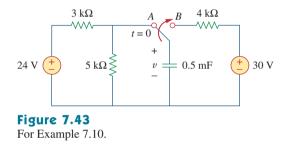
This is the same as saying that the complete response is the sum of the transient response and the steady-state response. the response using Eq. (7.53). This technique equally applies to *RL* circuits, as we shall see in the next section.

Note that if the switch changes position at time $t = t_0$ instead of at t = 0, there is a time delay in the response so that Eq. (7.53) becomes

$$v(t) = v(\infty) + [v(t_0) - v(\infty)]e^{-(t - t_0)/\tau}$$
(7.54)

where $v(t_0)$ is the initial value at $t = t_0^+$. Keep in mind that Eq. (7.53) or (7.54) applies only to step responses, that is, when the input excitation is constant.

The switch in Fig. 7.43 has been in position A for a long time. At t = 0, the switch moves to B. Determine v(t) for t > 0 and calculate its value at t = 1 s and 4 s.



Solution:

For t < 0, the switch is at position *A*. The capacitor acts like an open circuit to dc, but *v* is the same as the voltage across the 5-k Ω resistor. Hence, the voltage across the capacitor just before t = 0 is obtained by voltage division as

$$v(0^{-}) = \frac{5}{5+3}(24) = 15 \text{ V}$$

Using the fact that the capacitor voltage cannot change instantaneously,

$$v(0) = v(0^{-}) = v(0^{+}) = 15 \text{ V}$$

For t > 0, the switch is in position *B*. The Thevenin resistance connected to the capacitor is $R_{\text{Th}} = 4 \text{ k}\Omega$, and the time constant is

$$\tau = R_{\rm Th}C = 4 \times 10^3 \times 0.5 \times 10^{-3} = 2 \, {\rm s}$$

Since the capacitor acts like an open circuit to dc at steady state, $v(\infty) = 30$ V. Thus,

$$v(t) = v(\infty) + [v(0) - v(\infty)]e^{-t/\tau}$$

= 30 + (15 - 30)e^{-t/2} = (30 - 15e^{-0.5t}) V

At t = 1,

$$v(1) = 30 - 15e^{-0.5} = 20.9 \text{ V}$$

At t = 4,

$$v(4) = 30 - 15e^{-2} = 27.97$$
 V

Example 7.10

Practice Problem 7.10



Find v(t) for t > 0 in the circuit of Fig. 7.44. Assume the switch has been open for a long time and is closed at t = 0. Calculate v(t) at t = 0.5.

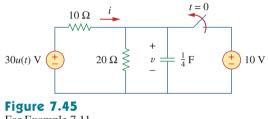
Answer:
$$(9.375 + 5.625e^{-2t})$$
 V for all $t > 0, 7.63$ V

For Practice Prob. 7.10.

Figure 7.44

Example 7.11

In Fig. 7.45, the switch has been closed for a long time and is opened at t = 0. Find *i* and *v* for all time.



For Example 7.11.

Solution:

The resistor current *i* can be discontinuous at t = 0, while the capacitor voltage *v* cannot. Hence, it is always better to find *v* and then obtain *i* from *v*.

By definition of the unit step function,

$$30u(t) = \begin{cases} 0, & t < 0\\ 30, & t > 0 \end{cases}$$

For t < 0, the switch is closed and 30u(t) = 0, so that the 30u(t) voltage source is replaced by a short circuit and should be regarded as contributing nothing to v. Since the switch has been closed for a long time, the capacitor voltage has reached steady state and the capacitor acts like an open circuit. Hence, the circuit becomes that shown in Fig. 7.46(a) for t < 0. From this circuit we obtain

$$v = 10 \text{ V}, \qquad i = -\frac{v}{10} = -1 \text{ A}$$

Since the capacitor voltage cannot change instantaneously,

$$v(0) = v(0^{-}) = 10 \text{ V}$$

For t > 0, the switch is opened and the 10-V voltage source is disconnected from the circuit. The 30u(t) voltage source is now operative, so the circuit becomes that shown in Fig. 7.46(b). After a long time, the circuit reaches steady state and the capacitor acts like an open circuit again. We obtain $v(\infty)$ by using voltage division, writing

$$v(\infty) = \frac{20}{20 + 10}(30) = 20 \text{ V}$$

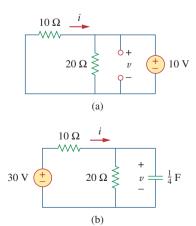


Figure 7.46 Solution of Example 7.11: (a) for t < 0, (b) for t > 0.

The Thevenin resistance at the capacitor terminals is

$$R_{\rm Th} = 10 \parallel 20 = \frac{10 \times 20}{30} = \frac{20}{3} \,\Omega$$

and the time constant is

$$\tau = R_{\rm Th}C = \frac{20}{3} \cdot \frac{1}{4} = \frac{5}{3} \, {\rm s}$$

Thus,

$$v(t) = v(\infty) + [v(0) - v(\infty)]e^{-t/\tau}$$

= 20 + (10 - 20)e^{-(3/5)t} = (20 - 10e^{-0.6t}) V

To obtain *i*, we notice from Fig. 7.46(b) that *i* is the sum of the currents through the 20- Ω resistor and the capacitor; that is,

$$i = \frac{v}{20} + C\frac{dv}{dt}$$

= 1 - 0.5e^{-0.6t} + 0.25(-0.6)(-10)e^{-0.6t} = (1 + e^{-0.6t}) A

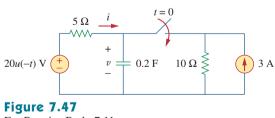
Notice from Fig. 7.46(b) that v + 10i = 30 is satisfied, as expected. Hence,

$$v = \begin{cases} 10 \text{ V}, & t < 0\\ (20 - 10e^{-0.6t}) \text{ V}, & t \ge 0 \end{cases}$$
$$i = \begin{cases} -1 \text{ A}, & t < 0\\ (1 + e^{-0.6t}) \text{ A}, & t > 0 \end{cases}$$

Notice that the capacitor voltage is continuous while the resistor current is not.

The switch in Fig. 7.47 is closed at t = 0. Find i(t) and v(t) for all time. **Pract** Note that u(-t) = 1 for t < 0 and 0 for t > 0. Also, u(-t) = 1 - u(t).

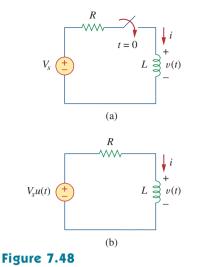
Practice Problem 7.11

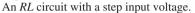




Answer:
$$i(t) = \begin{cases} 0, & t < 0 \\ -2(1 + e^{-1.5t}) \text{ A}, & t > 0, \end{cases}$$

 $v = \begin{cases} 20 \text{ V}, & t < 0 \\ 10(1 + e^{-1.5t}) \text{ V}, & t > 0 \end{cases}$





7.6 Step Response of an *RL* Circuit

Consider the *RL* circuit in Fig. 7.48(a), which may be replaced by the circuit in Fig. 7.48(b). Again, our goal is to find the inductor current i as the circuit response. Rather than apply Kirchhoff's laws, we will use the simple technique in Eqs. (7.50) through (7.53). Let the response be the sum of the transient response and the steady-state response,

$$i = i_t + i_{ss} \tag{7.55}$$

We know that the transient response is always a decaying exponential, that is,

$$i_t = A e^{-t/\tau}, \qquad \tau = \frac{L}{R}$$
(7.56)

where A is a constant to be determined.

The steady-state response is the value of the current a long time after the switch in Fig. 7.48(a) is closed. We know that the transient response essentially dies out after five time constants. At that time, the inductor becomes a short circuit, and the voltage across it is zero. The entire source voltage V_s appears across R. Thus, the steady-state response is

$$i_{ss} = \frac{V_s}{R} \tag{7.57}$$

Substituting Eqs. (7.56) and (7.57) into Eq. (7.55) gives

$$i = Ae^{-t/\tau} + \frac{V_s}{R} \tag{7.58}$$

We now determine the constant A from the initial value of *i*. Let I_0 be the initial current through the inductor, which may come from a source other than V_s . Since the current through the inductor cannot change instantaneously,

$$i(0^+) = i(0^-) = I_0 \tag{7.59}$$

Thus, at t = 0, Eq. (7.58) becomes

$$I_0 = A + \frac{V_s}{R}$$

From this, we obtain A as

$$A = I_0 - \frac{V_s}{R}$$

Substituting for A in Eq. (7.58), we get

$$i(t) = \frac{V_s}{R} + \left(I_0 - \frac{V_s}{R}\right)e^{-t/\tau}$$
 (7.60)

This is the complete response of the RL circuit. It is illustrated in Fig. 7.49. The response in Eq. (7.60) may be written as

$$i(t) = i(\infty) + [i(0) - i(\infty)]e^{-t/\tau}$$
 (7.61)

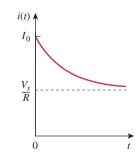


Figure 7.49 Total response of the *RL* circuit with initial inductor current I_0 .

where i(0) and $i(\infty)$ are the initial and final values of *i*, respectively. Thus, to find the step response of an *RL* circuit requires three things:

- 1. The initial inductor current i(0) at t = 0.
- 2. The final inductor current $i(\infty)$.
- 3. The time constant τ .

We obtain item 1 from the given circuit for t < 0 and items 2 and 3 from the circuit for t > 0. Once these items are determined, we obtain the response using Eq. (7.61). Keep in mind that this technique applies only for step responses.

Again, if the switching takes place at time $t = t_0$ instead of t = 0, Eq. (7.61) becomes

$$i(t) = i(\infty) + [i(t_0) - i(\infty)]e^{-(t-t_0)/\tau}$$
(7.62)

If $I_0 = 0$, then

$$i(t) = \begin{cases} 0, & t < 0\\ \frac{V_s}{R}(1 - e^{-t/\tau}), & t > 0 \end{cases}$$
 (7.63a)

or

$$i(t) = \frac{V_s}{R} (1 - e^{-t/\tau}) u(t)$$
 (7.63b)

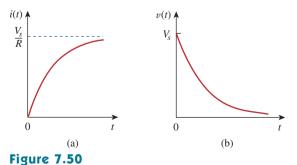
This is the step response of the *RL* circuit with no initial inductor current. The voltage across the inductor is obtained from Eq. (7.63) using v = L di/dt. We get

$$v(t) = L \frac{di}{dt} = V_s \frac{L}{\tau R} e^{-t/\tau}, \qquad \tau = \frac{L}{R}, \qquad t > 0$$

or

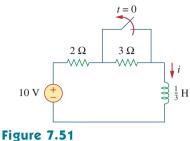
$$v(t) = V_s e^{-t/\tau} u(t)$$
 (7.64)

Figure 7.50 shows the step responses in Eqs. (7.63) and (7.64).



Step responses of an *RL* circuit with no initial inductor current: (a) current response, (b) voltage response.





For Example 7.12.

Find i(t) in the circuit of Fig. 7.51 for t > 0. Assume that the switch has been closed for a long time.

Solution:

When t < 0, the 3- Ω resistor is short-circuited, and the inductor acts like a short circuit. The current through the inductor at $t = 0^-$ (i.e., just before t = 0) is

$$i(0^{-}) = \frac{10}{2} = 5$$
 A

Since the inductor current cannot change instantaneously,

$$i(0) = i(0^+) = i(0^-) = 5$$
 A

When t > 0, the switch is open. The 2- Ω and 3- Ω resistors are in series, so that

$$i(\infty) = \frac{10}{2+3} = 2$$
 A

The Thevenin resistance across the inductor terminals is

$$R_{\rm Th} = 2 + 3 = 5 \,\Omega$$

For the time constant,

$$\tau = \frac{L}{R_{\rm Th}} = \frac{\frac{1}{3}}{5} = \frac{1}{15} \, {\rm s}$$

Thus,

$$i(t) = i(\infty) + [i(0) - i(\infty)]e^{-t/\tau}$$

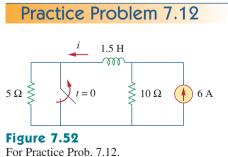
= 2 + (5 - 2)e^{-15t} = 2 + 3e^{-15t} A, t > 0

Check: In Fig. 7.51, for t > 0, KVL must be satisfied; that is,

$$10 = 5i + L\frac{di}{dt}$$

$$5i + L\frac{di}{dt} = [10 + 15e^{-15t}] + \left[\frac{1}{3}(3)(-15)e^{-15t}\right] = 10$$

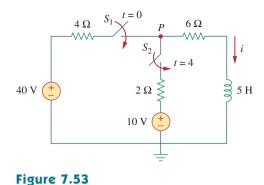
This confirms the result.



The switch in Fig. 7.52 has been closed for a long time. It opens at t = 0. Find i(t) for t > 0.

Answer: $(4 + 2e^{-10t})$ A for all t > 0.

At t = 0, switch 1 in Fig. 7.53 is closed, and switch 2 is closed 4 s later. Find i(t) for t > 0. Calculate *i* for t = 2 s and t = 5 s.



For Example 7.13.

Solution:

We need to consider the three time intervals $t \le 0, 0 \le t \le 4$, and $t \ge 4$ separately. For t < 0, switches S_1 and S_2 are open so that i = 0. Since the inductor current cannot change instantly,

$$i(0^{-}) = i(0) = i(0^{+}) = 0$$

For $0 \le t \le 4$, S_1 is closed so that the 4- Ω and 6- Ω resistors are in series. (Remember, at this time, S_2 is still open.) Hence, assuming for now that S_1 is closed forever,

$$i(\infty) = \frac{40}{4+6} = 4 \text{ A}, \qquad R_{\text{Th}} = 4 + 6 = 10 \Omega$$

 $\tau = \frac{L}{R_{\text{Th}}} = \frac{5}{10} = \frac{1}{2} \text{ s}$

Thus,

$$i(t) = i(\infty) + [i(0) - i(\infty)]e^{-t/\tau}$$

= 4 + (0 - 4)e^{-2t} = 4(1 - e^{-2t}) A, \qquad 0 \le t \le 4

For $t \ge 4$, S_2 is closed; the 10-V voltage source is connected, and the circuit changes. This sudden change does not affect the inductor current because the current cannot change abruptly. Thus, the initial current is

$$i(4) = i(4^{-}) = 4(1 - e^{-8}) \approx 4$$
 A

To find $i(\infty)$, let v be the voltage at node P in Fig. 7.53. Using KCL,

$$\frac{40 - v}{4} + \frac{10 - v}{2} = \frac{v}{6} \implies v = \frac{180}{11} \text{ V}$$
$$i(\infty) = \frac{v}{6} = \frac{30}{11} = 2.727 \text{ A}$$

The Thevenin resistance at the inductor terminals is

 $R_{\rm Th} = 4 \parallel 2 + 6 = \frac{4 \times 2}{6} + 6 = \frac{22}{3} \,\Omega$

and

$$\tau = \frac{L}{R_{\rm Th}} = \frac{5}{\frac{22}{3}} = \frac{15}{22} \,\mathrm{s}$$

Example 7.13

Hence,

$$i(t) = i(\infty) + [i(4) - i(\infty)]e^{-(t-4)/\tau}, \quad t \ge 4$$

We need (t - 4) in the exponential because of the time delay. Thus,

$$i(t) = 2.727 + (4 - 2.727)e^{-(t-4)/\tau}, \qquad \tau = \frac{15}{22}$$
$$= 2.727 + 1.273e^{-1.4667(t-4)}, \qquad t \ge 4$$
all this together

Putting all this together,

$$i(t) = \begin{cases} 0, & t \le 0\\ 4(1 - e^{-2t}), & 0 \le t \le 4\\ 2.727 + 1.273e^{-1.4667(t-4)}, & t \ge 4 \end{cases}$$

At t = 2,

Answer:

At
$$t = 5$$
,
 $i(2) = 4(1 - e^{-4}) = 3.93 \text{ A}$
 $i(5) = 2.727 + 1.273e^{-1.4667} = 3$

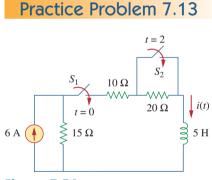


Figure 7.54 For Practice Prob. 7.13. Switch S_1 in Fig. 7.54 is closed at t = 0, and switch S_2 is closed at t = 2 s. Calculate i(t) for all t. Find i(1) and i(3).

3.02 A

2

$$i(t) = \begin{cases} 0, & t < 0\\ 2(1 - e^{-9t}), & 0 < t < \\ 3.6 - 1.6e^{-5(t-2)}, & t > 2 \end{cases}$$
$$i(1) = 1.9997 \text{ A}, i(3) = 3.589 \text{ A}.$$

7.7 [†]First-Order Op Amp Circuits

An op amp circuit containing a storage element will exhibit first-order behavior. Differentiators and integrators treated in Section 6.6 are examples of first-order op amp circuits. Again, for practical reasons, inductors are hardly ever used in op amp circuits; therefore, the op amp circuits we consider here are of the *RC* type.

As usual, we analyze op amp circuits using nodal analysis. Sometimes, the Thevenin equivalent circuit is used to reduce the op amp circuit to one that we can easily handle. The following three examples illustrate the concepts. The first one deals with a source-free op amp circuit, while the other two involve step responses. The three examples have been carefully selected to cover all possible *RC* types of op amp circuits, depending on the location of the capacitor with respect to the op amp; that is, the capacitor can be located in the input, the output, or the feedback loop.